



Form PTO-1449 (modified)	ATTY. DOCKET NO. MICS:0119 (03-0188)	SERIAL NO. 10/750,735
List of Patents and Publications For Applicant's Information Disclosure Statement	APPLICANT Duc V. Ho et al.	
(Use several sheets if necessary)	FILING DATE December 31, 2003	GROUP Unassigned

U.S. PATENT DOCUMENTS

EXAM. INIT.	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
MN	A	10/231,509		Gomm et al.			08/29/2002
MN	B	09/815,465		Gomm et al.			03/23/2001
	C						
	D						
	E						
	F						
	G						

FOREIGN PATENT DOCUMENTS

EXAM. INIT.	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION
	H						
	I						
	J						

OTHER ART

(Author, Title, Journal, Volume, Pertinent Pages, & Date)	
EXAMINER Matthew V. Nguyen	DATE CONSIDERED 12/16/05
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Information Disclosure Statement--PTO-1449 (Modified)

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U.S. PATENT DOCUMENTS

EXAM. INIT.	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
MN	A	6,586,979	07-01-2003	Gomm et al.			
MN	B	6,557,090	04-29-2003	Ho			
MN	C	6,240,028	05/29/2001	Ho			
	D						
	E						
	F						
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FOREIGN PATENT DOCUMENTS

EXAM. INIT.	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION
	I						
	J						
	K						

OTHER ART

(Author, Title, Journal, Volume, Pertinent Pages, & Date)		
MN	L	Gomm et al., Power Reduction For Delay Locked Loop Circuits, Published December 19, 2002
MN	M	Gomm et al., Method for Noise and Power Reduction For Digital Delay Lines; Published September 26, 2002
EXAMINER	DATE CONSIDERED	
Matthew V. Nguyen	12/16/05	

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